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| Substitute for form 1449A/PTO                           | Application Number           | 09/837,897   | 200            |
| FORMATION DISCLOSURE                                    |                              | April 18, 2001   | <del>,</del> , |
| TATEMENT BY APPLICANT                                   | First Named Inventor         | Allott, Stephen  | TA RO          |
| STATEMENT BY AFFLICANT                                  | Group Art Unit               | 2817   | 4              |
| (use as many sheets as necessary)                       | Examiner Name                | Unassigned   | -              |
| (use us many circum                                     | Attamay Docket Number        | 020408-002000US  |                |

Attorney Docket Number

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| Examiner<br>Signature |   | Date<br>Considered | · |

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|   | NFORMATION DISCLO                   | rining Date                            | April 18, 2001 2000          | <i>40]]</i>                             |
|   | STATEMENT BY APPLI                  | CANT First Name                        | ed Inventor Allott, Stephen  | · , , , , , , , , , , , , , , , , , , , |
| ١ |                                     | Group Art I                            | Jnit 2817                    | 100 AOO                                 |
|   | (use as many sheets as necess       | ery) Examiner N                        | Name Unassigned              |   |
| Ś | heet 2 of 2                         | Attorney D                             | ocket Number 020408-002000US |   |

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the Cite T<sup>2</sup> item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue Examiner number(s), publisher, city and/or country where published. No.1 Initials \* JOHNS, DAVID A, and MARTIN, KEN, "CMOS Transconductors Using Triode Transistors," Analog Integrated Circuit 1 Design, John Wiley & Sons, Inc., New York, 1997, pp 597-600. TSIVIDIS, YANNIS P., "Model Accuracy and Parameter Extraction," Operation and Modeling of the MOS Transistor. 2 McGraw-Hill, 1987, pp. 154-160. 

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